

2026 International Conference on

VLSI, Signal Processing, Power Electronics, IoT, Communication & Embedded Systems

(Under the aegis of ICETE 2026 - A Multiconference platform)

05 - 07, February 2026 | NMAM Institute of Technology (Nitte University) | Nitte, Mangalore, Karnataka, India.

VSPICE 2026 aims to foster innovation in various aspects of Electronics & Communication Engineering

About the conference

It provides a platform for researchers, professionals, executives, and engineers to share and exchange ideas on the conference theme. Keynote presentations, technical papers, and poster presentations will discuss new issues, address complex problems, and propose advanced solutions that can shape new trends and contribute to mankind's development.

Conference tracks

- ♦ VLSI, Embedded Systems & IoT
- ♦ Signal Processing
- ♦ Power Electronics
- ♦ Communication
- ♦ Artificial Intelligence, Machine Learning, Networking

Important dates

- Paper submission deadline
15, November 2025
- Acceptance Notification
25, December 2025
- Registration deadline
05, January 2026
- CRC deadline
15, January 2026

Call for papers

VSPICE 2026 invites full length original research contributions from engineering professionals from industries, R&D organisations, academic institutions, government departments and research scholars from across the world. Full length original research contributions and review articles not exceeding TEN pages as per the Springer's single column format shall be submitted. The manuscript should contribute original research ideas, developmental ideas, analysis, findings, results, etc. The manuscript should not have been published in any journals/magazines or conference proceedings and not under review in any of them. Further the manuscript should contain the name of the corresponding author with e-mail id and affiliation of all authors.

Paper submission

Authors shall submit their full length original research/review papers to vspice@nitte.edu.in

Publications

All peer reviewed and selected papers of the conference will be published in a separate volume and the papers presented at the conference will be published in a **Scopus indexed AIP Conference Proceedings**. Also, presented papers will be considered for publication in the Springer's Lecture Notes in Electrical Engineering (proposal is under process)

For more information

Conference website : <https://vspice-nitte.in/> | Conference e-mail : vspice@nitte.edu.in

Organizer



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Paper submission

To submit papers, you can log into your CMT at : <https://cmt3.research.microsoft.com/VSPICE2026>

Scope and Theme

Advances in VLSI, Signal Processing, Image Processing, Machine Learning, Power Electronics, IoT, Communication, and Embedded Systems pave a pathway for the development of efficient and economical systems. For making the best use of resources, expertise in multiple specializations and interdisciplinary research is gaining prominence. Hence, the objective of the conference is to provide a platform for people who are working in Electronics, Communication, and allied disciplines to share their expertise and contribute to technical innovations.

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