

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INTERNATIONAL CONFERENCE ON “VLSI, SIGNAL PROCESSING, POWER ELECTRONICS, IoT, COMMUNICATION AND EMBEDDED SYSTEMS”

5-7 February 2026

SCHEDULE

05- 02- 2026			
8.30 – 9.15 AM	Registration		
9.15 – 11.00 AM	Inaugural Function		Sambhram
11.00 – 11. 30AM	TEA BREAK		
11.30 – 1. 00 PM	Key Note Address		Nethravathi Seminar Hall
1.00 – 2.00 PM	LUNCH BREAK		
2.00- 4.00 PM	Session 1	Paper ID: ICE1003, 1006, 1016, 1030, 1038, 1040, 1043, 1055	Nethravathi Seminar Hall
7.00 PM Onwards	BANQUET DINNER		
06- 02- 2026			
9:30P.M.- 11A.M	Session 2	Paper ID:ICE1048, 1051, 1059, 1060, 1061, 1063, 1064, 1099	Nethravathi Seminar Hall
	TEA BREAK		
1.00 – 2.00 PM	LUNCH BREAK		

Dr. K.V.S.S.S.S. Sairam
Program Chair